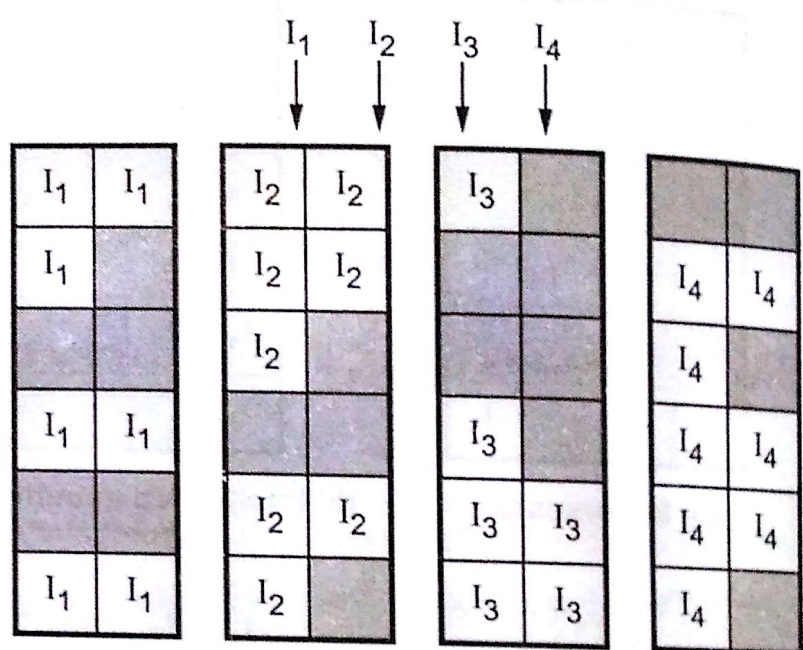
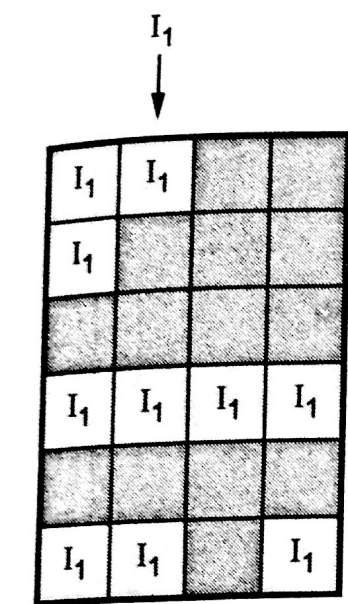


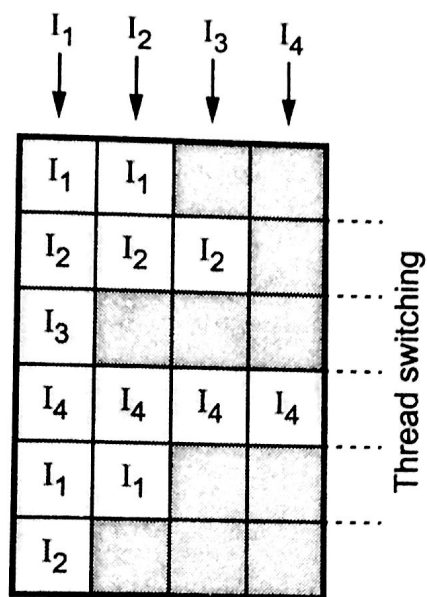
(a) Simultaneous multithreading (SMT)



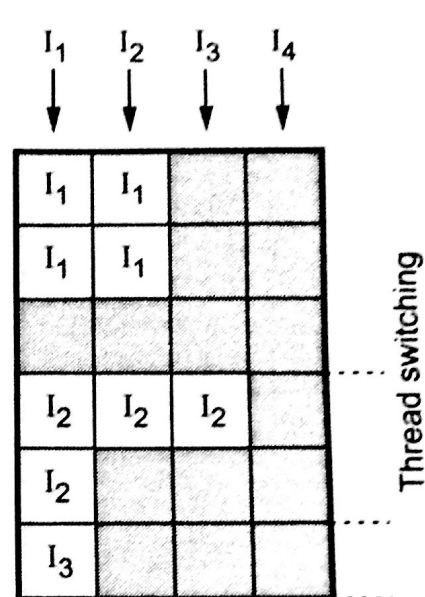
(b) Chip multiprocessor (Multicore)



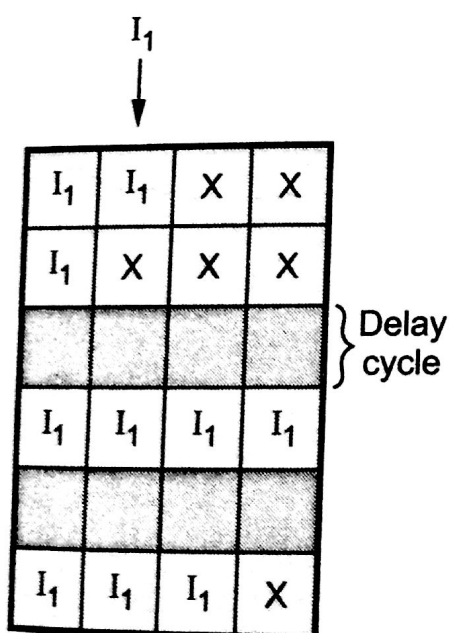
(a) Superscalar



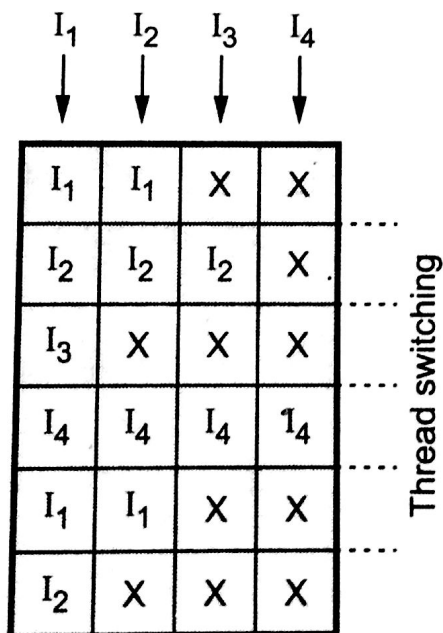
(b) Interleaved multithreading superscalar



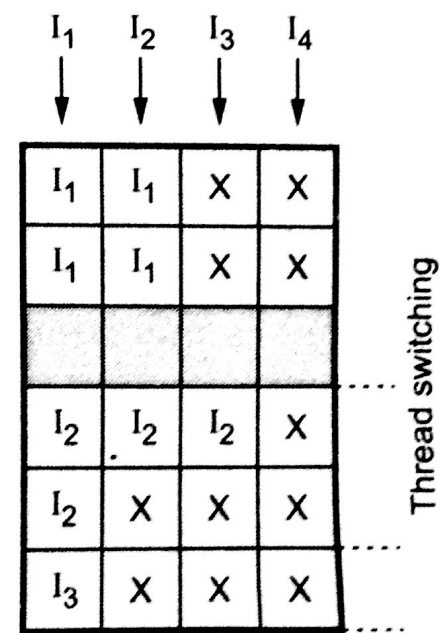
(c) Blocked multithreading superscalar



(d) VLIW



(e) Interleaved multithreading VLIW



(f) Blocked multithreading VLIW

Fig. 4.4.2 Approaches to execute multiple threads

pipeline is used like in RISC and CISC machines.

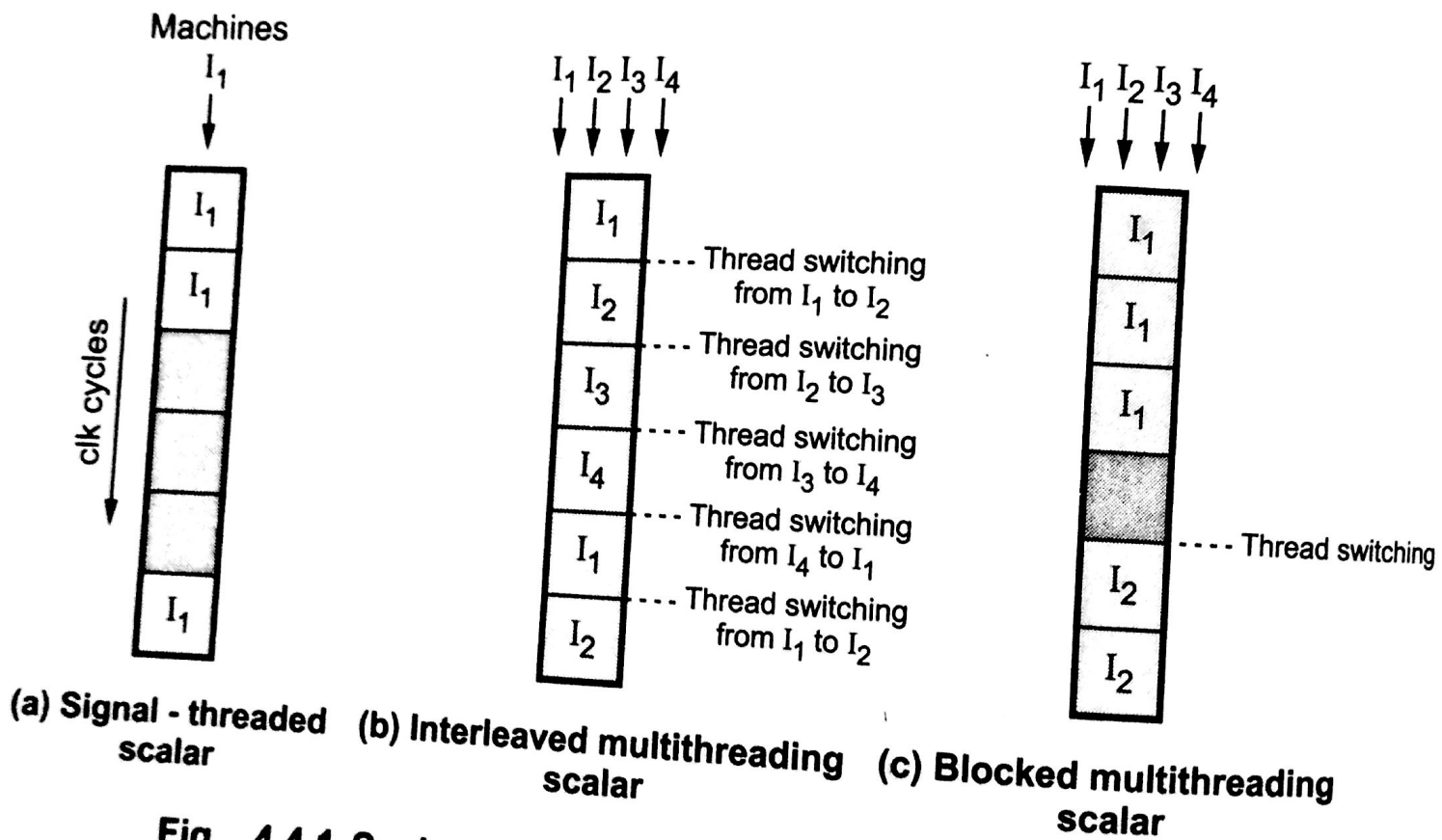


Fig. 4.4.1 Scalar approaches for threads scalar execution